

**Memory Address Decoding Method And Related Apparatus By Bit-Pattern
Matching**

Appl. No.	:	10/708,103	Confirmation No.	2102
Applicant	:	Jacky Tsai		
Filed	:	February 10, 2004		
TC/A.U.	:	2186		
Examiner	:	PATEL, HETUL B		
Docket No.	:	VIAP0083USA0		
Customer No.	:	27765		

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

AMENDMENT

5 Sir:

In response to the Office action of September 7, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

10 **Remarks/Arguments** begin on page 5 of this paper.